

Maxim > Design Support > Technical Documents > Application Notes > A/D and D/A Conversion/Sampling Circuits > APP 2394

Maxim > Design Support > Technical Documents > Application Notes > Interface Circuits > APP 2394

Maxim > Design Support > Technical Documents > Application Notes > Voltage References > APP 2394

Keywords: assembly, adc, a/d, a-d, 2-wire, serial interface, I2C, 16-bit, analog to digital converter, ADCs, pic microcontroller, PIC, microcontrollers, micros

APPLICATION NOTE 2394

Interfacing the MAX1169 ADC to a PIC Microcontroller

By: Ted Salazar, Applications Engineering Manager Sep 29, 2003

Abstract: This application note describes how to interface a MAX1169 analog-to-digital converter (ADC) to a PIC® microcontroller. It includes an example circuit and software for a PIC18F442. The software contains function calls to interface the ADC to the PIC microcontroller using the internal MSSP I²C port at 400kHz.

The MAX1169 is a 16-bit, low power ADC featuring a 2-Wire I²C compatible serial interface. The MAX1169's interface is capable of supporting both Fast Mode (400kHz) and High Speed Mode (up to 1.7MHz).

This application note consists of an example application circuit and software using the PIC18F442. The software contains function calls to interface the MAX1169 to the PIC using the internal MSSP I²C port at 400kHz. Since other microcontrollers have similar on-chip peripherals, the example I²C communication routines provided were purposely broken down into single I²C function calls so that the code can be quickly implemented with other microcontrollers. **Table 1** shows the single I²C compatible function calls used in the assembly program example.

The application circuit uses the MAX1169EVKIT, which consists of the MAX1169, an input buffer (MAX4430) and a proven PCB layout. The PIC and RS-232 transceiver IC (MAX3232) are not present on the MAX1169EVKIT. The SCL and SDA pads on the MAX1169EVKIT allow an easy connection for the I²C compatible serial interface. Install 1.5kΩ pull-up resistors on R3 and R4 for 400kHz I²C operation.

The pinout of the MAX1169 allows easy isolation of the analog section from the digital section. The analog section is grouped together on pins 8-13 on the right side of the IC and the digital section is located on pins 1-7 on the left side of the IC. Pin 14 is also part of the digital section but can be easily grouped with the left side of the IC. For optimum performance it is recommended to use separate analog and digital supplies as shown in the schematic in **Figure 1**.

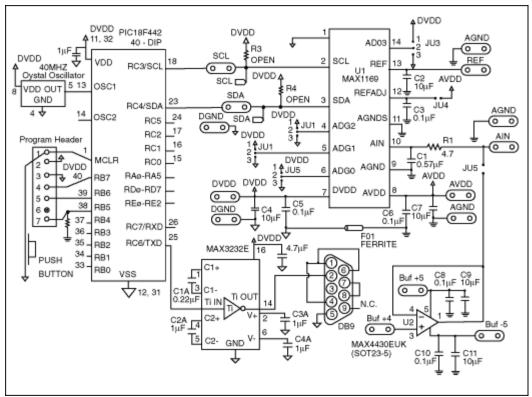
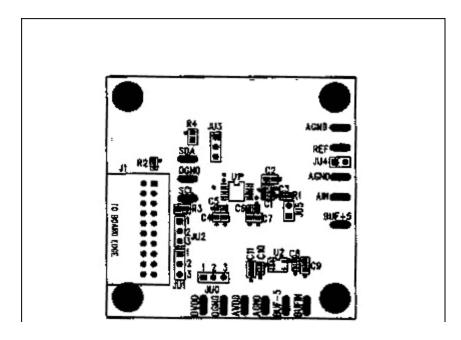


Figure 1. MAX1169 application schematic.

Separate the analog and digital grounds as shown in **Figure 2**. Connect both ground planes near the MAX1169 through a ferrite bead. Using a ferrite bead such as the TDK MMZ1608B601C to connect both grounds prevents the microcontroller's system clock and its harmonics from feeding into the analog ground. The MMZ1608B601C was chosen for its specific impedance versus frequency characteristics knowing the system clock of the PIC18F442 is 40MHz. **Figure 3** shows the impedance versus frequency curve for the MMZ1608B601C.



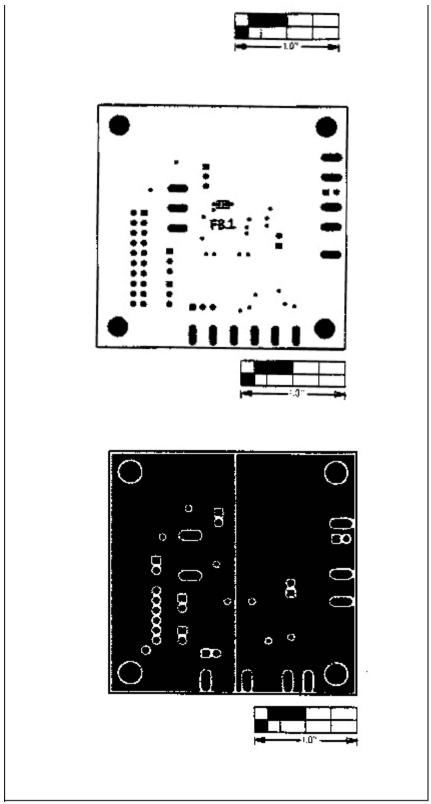


Figure 2. Separating the analog and digital grounds.

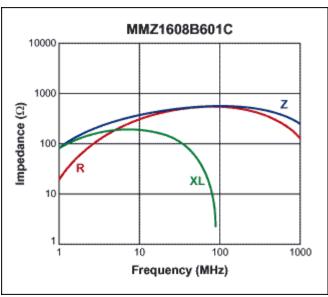


Figure 3. Impedance versus frequency curve for the TDK MMZ1608B601C ferrite bead.

The example assembly program reads continuous conversions from the MAX1169 16-bit ADC to the PIC at a 2-Wire interface frequency of 400kHz. Once the digitized analog voltage representation of the data is received by the PIC, it is immediately transmitted out by a software UART at 115kbps. The RS-232 transceiver allows the data to be captured by a personal computer with a standard serial port.

Table 1. Single I²C compatible function calls.

rable 1. Onlyie 10 compatible function cans.		
Single I ² C compatible function calls	Description	
HWI ² C_start	Performs an I ² C compatible start condition.	
HWI ² C_repeatedStart	Performs an I ² C compatible repeated start condition.	
HWI2C_W_slaveAddr	Performs all I ² C compatible write operations including the initial slave address write frame.	
HWI2C_readMore_W	Performs an I ² C compatible read operation with Master ACK.	
HWI2C_readLast_W	Performs an I ² C compatible read operation with Master NACK.	
HWI ² C_stop	Performs an I ² C compatible stop condition.	

Download an assembly example program that interfaces the MAX1169 to the PIC18F442's internal MSSP I²C peripheral.

PIC is a registered trademark of Microchip Technology Incorporated.

Related Parts		
MAX1169	58.6ksps, 16-Bit, 2-Wire Serial ADC in a 14-Pin TSSOP	Free Samples
MAX3232	3.0V to 5.5V, Low-Power, up to 1Mbps, True RS-232 Transceivers Using Four 0.1µF External Capacitors	Free Samples

MAX4430 Dual-Supply, 180MHz, 16-Bit Accurate, Ultra-Low-

Distortion Op Amps

Free Samples

More Information

For Technical Support: http://www.maximintegrated.com/support

For Samples: http://www.maximintegrated.com/samples

Other Questions and Comments: http://www.maximintegrated.com/contact

Application Note 2394: http://www.maximintegrated.com/an2394

APPLICATION NOTE 2394, AN2394, AN 2394, APP2394, Appnote 2394, Appnote 2394

Copyright © by Maxim Integrated Products

Additional Legal Notices: http://www.maximintegrated.com/legal